mov eax, i

or eax, d

mov ecx, DWORD PTR \_i$[ebp]

mov DWORD PTR ?A@@3PAHA[ecx\*4], eax

mov eax, DWORD PTR \_i$[ebp]

mov ecx, DWORD PTR ?d@@3HA

cmp d, 10

add ecx, DWORD PTR ?A@@3PAHA[eax\*4]

mov DWORD PTR ?d@@3HA, ecx

jle SHORT $LN7@main

mov eax, 127

sub eax, DWORD PTR \_i$[ebp]

mov ecx, DWORD PTR \_i$[ebp]

mov DWORD PTR ?A@@3PAHA[ecx\*4], eax

mov eax, DWORD PTR \_i$[ebp]

add eax, 1

mov DWORD PTR \_i$[ebp], eax

jmp SHORT $LN2@main

\_asm{

mov eax, i

or eax, d

mov ecx, d

add ecx, eax

mov d, ecx

cmp d, 10

jle SHORT LN4main

mov eax, 127

sub eax, i

LN4main:

mov ecx, i

shl ecx,2

mov A[ecx], eax

add eax, 1

mov i, eax}